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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
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09/589,919	06/07/2000	Zhiwu Liu	0325.00374	8518
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21363 7590 05/09/2002

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EXAMINER

WHITMORE, STACY

ART UNIT	PAPER NUMBER
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2812

DATE MAILED: 05/09/2002

Please find below and/or attached an Office communication concerning this application or proceeding.

<b>Office Action Summary</b>	Application No. 09/589,919	Applicant(s) LIU, ZHIWU	
	Examiner Stacy A Whitmore	Art Unit 2812	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

**Period for Reply**

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133).
- Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

**Status**

- 1) ☒ Responsive to communication(s) filed on 07 June 2000.
- 2a) ☐ This action is **FINAL**.                      2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

**Disposition of Claims**

- 4) ☒ Claim(s) 1-20 is/are pending in the application.
- 4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.
- 5) ☐ Claim(s) \_\_\_\_\_ is/are allowed.
- 6) ☒ Claim(s) 1-20 is/are rejected.
- 7) ☐ Claim(s) \_\_\_\_\_ is/are objected to.
- 8) ☐ Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

**Application Papers**

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on \_\_\_\_\_ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.  
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
- 11) ☐ The proposed drawing correction filed on \_\_\_\_\_ is: a) ☐ approved b) ☐ disapproved by the Examiner.  
If approved, corrected drawings are required in reply to this Office action.
- 12) ☐ The oath or declaration is objected to by the Examiner.

**Priority under 35 U.S.C. §§ 119 and 120**

- 13) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).  
a) ☐ All b) ☐ Some \* c) ☐ None of:  
1. ☐ Certified copies of the priority documents have been received.  
2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.  
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).  
\* See the attached detailed Office action for a list of the certified copies not received.
- 14) ☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. § 119(e) (to a provisional application).  
a) ☐ The translation of the foreign language provisional application has been received.
- 15) ☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. §§ 120 and/or 121.

**Attachment(s)**

- |  |   |
|--|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892)                                  | 4) <input type="checkbox"/> Interview Summary (PTO-413) Paper No(s). _____  |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948)                         | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| 3) <input checked="" type="checkbox"/> Information Disclosure Statement(s) (PTO-1449) Paper No(s) <u>2</u> . | 6) <input type="checkbox"/> Other: _____                                    |

## DETAILED ACTION

### *Claim Objections*

1. Claim 6 is objected to because of the following informalities:  
In claim 6, line 8, the term multiplier should read mulitplexer.  
Appropriate correction is required.

### *Claim Rejections - 35 USC § 102*

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

2. Claims 1-4, 6-16, and 18-20 are rejected under 35 U.S.C. 102(b) as being anticipated by Applicant's Admitted Prior Art (hereinafter referred to as AAPA).
3. As for claims 1 and 13, AAPA taught the invention as claimed, including an apparatus comprising:  
a circuit comprising [means for] one or more inputs configured to provide a device ID of one or more different device ID's, wherein said one or more inputs allow said circuit to be implemented with one of said one or more different device ID's [pg. 1, lines 11-20].
4. As for claim 2, AAPA taught said device ID comprises a soft code [pg. 1, line 16].
5. As for claim 3, AAPA taught the circuit comprises a JTAG controller [pg. 1 – pg. 3, line 17].

6. As for claim 4, AAPA taught wherein each of said one or more different device ID's comprise a configuration of said circuit [pg. 1 – pg. 3, line 17].
7. As for claim 6, AAPA further taught  
a logic circuit configured to receive one or more inputs [pg. 1 – pg. 3, line 17, and fig. 1];  
a multiplier configured to receive an output of said logic circuit [pg. 1 – pg. 3, line 17, and fig. 1]; and  
a memory element configured to receive an output of said multiplexer [pg. 1 – pg. 3, line 17, and fig. 1].
8. As for claim, 7, AAPA taught the multiplexer is configured to receive an input signal and a shift signal [pg. 1 – pg. 3, line 17, and fig. 1, especially element 22].
9. As for claim 8, AAPA taught said logic circuit comprises a logic gate [pg. 1 – pg. 3, line 17, and fig. 1, especially pg. 2, line 4].
10. As for claim 9, AAPA taught said circuit is implemented with a FIFO memory [pg. 1 – pg. 3, line 17, and fig. 1].
11. As for claim 10, AAPA taught said one or more inputs comprise mark options [pg. 3].
12. As for claim 11, AAPA taught said one or more inputs comprise configuration input pins [pgs. 1-3].
13. As for claim 12, AAPA taught said circuit comprises a JTAG device compliant with the IEEE standard 1149.1 [pgs. 1-3].
14. As for claim 14, AAPA taught a method for multiple device ID's comprising the steps of:  
receiving one or more inputs [pgs. 1-3];  
providing a device ID of one or more different device IDs, wherein said one or more inputs allow implementation of said one or more different device IDs [pgs. 1-3].

15. Claims 15-16, and 18-20, have similar limitations as claims 2,4, and 10-12, and are rejected for the same reasons as cited in the rejections of claims 2,4, and 10-12.

***Claim Rejections - 35 USC § 103***

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

16. Claims 5, and 17 are rejected under 35 U.S.C. 103(a) as being unpatentable over AAPA as applied to claims 1, 6, and 14 above, and further in view of Swoboda (5,828,824) or Carmichael (6,308,311).

17. As for claims 5, and 17, AAPA taught the apparatus and method of multiple device IDs as cited above in the rejections of claims 1, 6, and 14. AAPA did not specifically teach wherein the device ID can be configured after fabrication (of said apparatus). However, Swoboda taught the device ID can be configured after fabrication [abstract, fig. 2, col. 4, line 65 – col. 5, line 8, col. 6, line 24]. Further more Carmichael taught the device ID can be configured after fabrication [col. 2, line 66 – col. 4, especially col. 4, lines 7-23]. It would have been obvious to one of ordinary skill in the art at the time the invention was made to combine the teachings of (AAPA and Swoboda) or (AAPA and Carmichael) because configuring the device DI after fabrication would enable convenient test and or device reconfiguration which would enable better testing facilities or communications with a host system [see Swoboda, col. 6, and Carmichael, col. 4].

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18. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Stacy A Whitmore whose telephone number is (703) 305-0565. The examiner can normally be reached on Monday-Thursday, alternate Friday 6:30am - 4:00 pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, John Niebling can be reached on (703) 308-3325. The fax phone numbers for the organization where this application or proceeding is assigned are (703) 308-7724 for regular communications and (703) 308-7724 for After Final communications.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is (703) 308-0956.

Stacy Whitmore  
May 2, 2002



RICHARD BOOTH  
PRIMARY EXAMINER